



Mariya Brindha J

Citizenship : India ▪ Date of birth : 11 april,1992

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Profile

Objective An Engineer by passion, seeking a Career within the Information Technology domain, where in my core analytical skills along with my technical skills can be utilized.

Aspirations To scale up, not just in position and years of experience, but also to scale up my thoughts, skills and personality to suit my level in industry. To give life to my dreams and be an inspiration to my associates.

Key Skills

Proficient or familiar with a vast array of programming languages, concepts and technologies, including:

**C,C++
OOPS concepts**

MS Office

Education

- | | |
|---------------------|--|
| 2013 to 2015 | Master in Engineering (April 2015) – “Applied Electronics”
COLLEGE NAME, Veeramal Engineering College, Dindigul – 81% |
| 2009 to 2013 | Bachelor in Engineering (April 2013)–“Electronics & Communication Engineering”
COLLEGE NAME, R.V.S College of Engineering and Technology, Dindigul – 80% |
| 2008 to 2009 | Higher Secondary – “ Computer Science ”
SCHOOL NAME, Holy Cross Girls Hr. Sec. School, Batlagundu– 87.5% |
| 2006 to 2007 | “ General ”
SCHOOL NAME, Holy Cross Girls Hr. Sec. School, Batlagundu – 87.6% |

Capabilities and Skills

- ✓ Communication: An extrovert by nature, I can easily strike a conversation with a person. I am continuously working on my English skills, so as to prepare myself for the workplace environment. An excellent listener, I believe in “listen first and talk next” philosophy.
- ✓ Responsibility I work towards gaining the confidence of my professors. I take pride in mentioning that, my teachers entrust me in most of the important activities concerned with the department, such as a coordination from students in organizing a function or a simple attendance role call etc.
- ✓ Conceptual Thinking: I am also a believer of facts and not preaching. I try to understand a concept, before learning it just for the sake of it.
- ✓ Open to improvement: I believe that improvement is a continuous process and I believe in self competition to keep improving myself

Industry Exposure and Projects

M.E. Project

- ✓ I have done my project in Wireless Network domain “Cooperative Key Agreement for wireless networking using ECDH algorithm”
- ✓ By utilizing the broadcast nature of radio channels, cooperative wireless communications exploits a new degree of freedom to combat unfavorable wireless channel conditions with the assistance of relays.
- ✓ Here focus on a cooperative wireless system where there is an authenticated and honest relay. Passive adversaries, i.e., eavesdroppers, are assumed in this system.
- ✓ The case when the eavesdropper is collocated with the relay, which is shown to be the worst case eavesdropper location based on the source-to-eavesdropper channel signal-to-noise ration(SNR), is studied.
- ✓ Lower and upper bounds on the secret key rate between the two legitimate communicants with respect to the information obtained by the eavesdropper are derived for specific modulation and SNR conditions.
- ✓ The design of a key agreement protocol based on a novel and simple advantage distillation scheme that exploits the physical channel randomness is studied.
- ✓ Design parameters are determined based on key rate bounds to optimize the efficiency of the protocol while ensuring secrecy.
- ✓ Key extraction based on channel randomness, which relies on channel reciprocity and estimation of the channel response.
- ✓ Conversely, the key agreement protocol proposed in this paper generates key bits from a random sequence transmitted with standard modulation schemes.
- ✓ As a result, our proposed scheme has lower implementation complexity.

B.E. Project

- ✓ I have done my project in VLSI domain “Virus Detection Processor for Network Security using AC with merge FSM”
- ✓ Among hardware approaches, memory-based architecture has attracted a lot of attention because of its easy configurability and scalability. Due to the increasing complexity of network traffic and the growing number of attacks, an intrusion detection system must be efficient, flexible and scalable.
- ✓ In order to accommodate the increasing number of attack patterns and meet the throughput requirement of networks, a successful network intrusion detection system must have a memory – efficient pattern-matching algorithm and hardware design.
- ✓ The MERGE_FSM pattern-matching algorithm will significantly reduce the memory requirement. The goal of this work is to provide a systematic virus detection hardware solution for network security for embedded systems.

Implant Training @ All India Radio Station, Madurai

- ✓ Was trained about the operational processes and techniques involved in telecasting a programme.
- ✓ Understood the concepts of communication through a media, on live account, which helped me in visualizing what was only available as texts in books.
- ✓ Was able to understand and appreciate the importance of “processes” and methodology, and the efficiency factor it brings in achieving the quality of the work in any industry/ organization like Radio

Paper Presentation @ Sri Ramakrishna Engineering College, Coimbatore

- ✓ I have participated paper presentation about "4G Mobile Communication"

Subject Interests and Proficiency

- ✓ **Digital Electronics**
- ✓ **Microprocessor**
- ✓ **Signals and system**

Accolades

- ✓ .Awarded for punctuality and regularity (0% tardiness) in college
- ✓ General proficiency prizes @ school level.

Activities and Interests

Hobbies: Gardening. Cooking

Interests: Listening to music, Movies and spending time with friends

Languages

Tamil (native)

English (fluent)

Personal Details

Husband Name C.John Ezhilarasu

Date Of Birth 11 April 1992

Marital Status Married

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